

**PATENT**  
**IBM Docket No. RAL920000070US1**

**Amendments to the Claims:**

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1. (Original) A method comprising:  
    providing a set of transactions characterizing a bus architecture;  
    selecting a subset of said set of transactions applicable to a design-under-test (DUT); and  
    generating a configuration file for verifying said DUT from said subset.
  2. (Original) The method of claim 1, further comprising generating a test case comprising bus transactions from said configuration file.
  3. (Original) The method of claim 2, further comprising applying said test case to said DUT to verify said DUT.
  4. (Original) A method comprising:  
    providing a comprehensive set of bus transactions characterizing a bus architecture;  
    creating a configuration file specific to a DUT from said comprehensive set; and  
    generating parameterized bus transactions from said configuration file.
  5. (Currently Amended) The method of claims 1 or 4, further comprising:  
    providing a user interface which presents a user with possible transaction types for said bus architecture;  
    entering inputs corresponding to a specific DUT via said interface; and  
    processing said inputs to generate said configuration file.

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6. (Currently Amended) ~~The method of claim 5;~~ A method comprising:

providing a comprehensive set of bus transactions characterizing a bus architecture;

creating a configuration file specific to a DUT from said comprehensive set;

generating parameterized bus transactions from said configuration file;

providing a user interface which presents a user with possible transaction types for said bus architecture;

entering inputs corresponding to a specific DUT via said interface; and

processing said inputs to generate said configuration file wherein said processing step comprises including rules corresponding to said inputs in said configuration file to select bus transactions specific to said DUT.

7. (Original) The method of claim 5, wherein said inputs specify parameters for application to said specific DUT.

8. (Original) The method of claim 4, further comprising applying said parameterized transactions to said DUT to determine compliance of said DUT with said bus architecture.

9. (Original) A method comprising:

providing a table having entries corresponding to a set of transactions of a bus architecture;

applying inputs corresponding to a specific DUT to said table;

modifying said entries using said inputs; and

generating a configuration file for verifying said DUT from said modified entries.

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10. (Original) The method of claim 9 wherein said applying is performed with a user interface.
11. (Original) The method of claim 9 further comprising generating a DUT test case from said configuration file.
12. (Original) A computer-usable medium storing computer-executable instructions which when executed implement a process comprising:
- applying selected parameters for verifying a DUT to a table including a set of transactions characterizing a bus architecture;
  - modifying entries in said table in accordance with said parameters; and
  - generating a configuration file for verifying said DUT from said modified entries.
13. (Currently Amended) ~~The computer-usable medium of claim 12, said process further comprising~~ A computer-usable medium storing computer-executable instructions which when executed implement a process comprising:
- applying selected parameters for verifying a DUT to a table including a set of transactions characterizing a bus architecture;
  - modifying entries in said table in accordance with said parameters;
  - generating a configuration file for verifying said DUT from said modified entries;
- and
- generating rules from said modified entries for selecting bus transactions specific to said DUT.
14. (Original) The computer-usable medium of claim 12, said process further comprising processing said configuration file to create a test case for said DUT.

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15. (Original) A system comprising:

a memory including computer-executable instructions;

a processor coupled to said memory for executing said instructions; and

a table including transactions characterizing a bus architecture;

wherein said instructions support a user interface for entering inputs to generate a specific DUT configuration file from said table, said configuration file including parameters and rules for verifying said DUT.

16. (Currently amended) ~~The system of claim 15, wherein said instructions~~ A system comprising:

a memory including computer-executable instructions;

a processor coupled to said memory for executing said instructions; and

a table including transactions characterizing a bus architecture;

wherein said instructions support a user interface for entering inputs to generate a specific DUT configuration file from said table, said configuration file including parameters and rules for verifying said DUT and process entries in said table modified by said inputs to generate a syntax for said configuration file.

17. (Original) The system of claim 16, wherein said instructions parse said syntax to generate a test case for verifying said DUT.

18. (New) The method of claim 9 further comprising providing a user interface which presents a user with possible transaction types for said bus architecture; entering inputs corresponding to a specific DUT via said interface; and processing said inputs to generate said configuration file.

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